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Bib Data Sheet

CONFIRMATION NO. 8605

SERIAL NUMBER 10/634,839	FILING DATE 08/06/2003 RULE	CLASS 257	GROUP ART UNIT 2814	ATTORNEY DOCKET NO. 021331A
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APPLICANTS

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** CONTINUING DATA ****

This application is a DIV of 10/289,295 11/07/2002 PAT 6,639,280

** FOREIGN APPLICATIONS ****

JAPAN 2002-008742 01/17/2002

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 11/06/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 4	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 1
Verified and Acknowledged Examiner's Signature		Initials			

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TITLE

Manufacturing method of semiconductor device and semiconductor chip using SOI substrate

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